INTDEC 10 version 1.03

The following CVs are programmable in the decoder. Some are placeholders in this version, as their function is not implemented.

CV #	Name	Functional?	Value range/ Description
1	Primary Address	Y	0 – 127
2	Start Voltage	Y	0 – 255; 0,1 default to 7
3	Acceleration Rate	Y	Time between accel. Steps, in 3.5ms units (.895sec / 256)
4	Deceleration Rate	Y	Time between decel. Steps, in 3.5ms units (.895sec / 256)
5	High V	Y	0 – 255; 0,1 default to 255
6	Mid. V	Y	0 – 255; 0,1 default to 128
7	Version	Y	103
8	Man. ID	Y	Any
9	PWM Total Period	Y	0-31; T = CV9 * 4 + 131 uSec
10	EMF Cutoff	N	
11	Packet Timeout	Y	Timeout for not receiving valid packets, in seconds.
17	Extended Address MSB	Y	0xC0 – 0xE7
18	Extended Address LSB	Y	0 – 0xFF
19	Consist Address	Y	0 – 255 (high bit controls relative direction)
21	Consist control Fn's	Y	0 – 255
22	Consist control Lamps	Y	0 – 3
23	Accel. Adjustment	Y	0 – 255; see CV3
24	Decel. Adjustment	Y	0 – 255; see CV4
25	Cab Speed Step	N	

29	Config 1	Y	All bits are functional (adv. Ack not defined); bit 6 – motor damping
30	Error Info	N	
31	Config 2	N	
32	Config 3	N	
33 – 37	Function control Output	Y	CV33 = FL; CV34 = RL; CV35 = F1; CV36 = F2; CV37 = F3; output bits are defined for the HW; in case of DECOD10: Bit 0 – Out 2 Bit 1 – Out 3 Bit 2 – unused Bit 3 – unused Bit 4 – Out Fwd Light Bit 5 – Out Back Light Bit 6 – Out 1 Bit 7 – unused
49 – 53	Special Effects	Y	For functions FL, RL, F1-F3 respectively Bit 0 – Dim (light) Bit 1 – Strobe Bit 2 – MARS Bit 3 – Active in Rev. Bit 4 – Active in Fwd. Bit 5 - .25sec Phase A Bit 6 - .25sec Phase B Bit 7 – speed related pulse (chaff)
65	Kick Start V	N	
66	Fwd Trim	N	
67 – 94	Speed Table	Y	0 – 255
95	Rev. Trim	N	